ONFI Technical Errata

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| **Errata ID** | **001** |
| **Affected Spec Ver.** | **ONFI 5.0/5.1/5.2** |
| **Corrected Spec Ver.** |  |

Submission info

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| Name | Company | Date |
| Kai-Uwe Schmidt | Solidigm/SKH | 05/30/24 |

This erratum updates the Table 2-6 to correct missing and incorrect signal mappings.

| **Signal Name** | **M/O/R** | **BGA-178 SDR x8** | **BGA-178 NV-DDR x8** | **BGA-178 NV-DDR2/3 x8** | **BGA-178 NV-LPDDR4 x8** | **BGA-154 SDR x8** | **BGA-154 NV-DDR x8** | **BGA-154 NV-DDR2/3 x8** | **BGA-154 NV-LPDDR4 x8** | **BGA-146 SDR x8** | **BGA-146 NV-DDR x8** | **BGA-146 NV-DDR2/3 x8** | **BGA-146 NV-LPDDR4 x8** |
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| R/B0\_0\_nR/B0\_1\_nR/B1\_0\_nR/B1\_1\_n | MOOO | F8K8G8J8 | F8K8G8J8 | F8K8G8J8 | F8K8G8J8 | F7K7G7J7 | F7K7G7J7 | F7K7G7J7 | F7K7G7J7 | H8H4H7H5 | H8H4H7H5 | H8H4H7H5 | H8H4H7H5 |
| RE\_0\_n (t)RE\_1\_n (t) | MO | G4J12 | nana | G4J12 | G4J12 | G3J11 | nana | G3J11 | G3J11 | D7M5 | nana | D7M5 | D7M5 |
| RE\_0\_cRE\_1\_c | OO | nana | nana | G5J11 | G5J11 | nana | nana | G4J10 | G4J10 | nana | nana | E7L5 | E7L5 |
| W/R\_0\_nW/R\_1\_n | MO | nana | G4J12 | nana | nana | nana | G3J11 | nana | nana | nana | D7M5 | nana | nana |
| CE0\_0\_nCE0\_1\_nCE1\_0\_nCE1\_1\_nCE2\_0\_nCE2\_1\_nCE3\_0\_nCE3\_1\_n | MOOOOOOO | E8L8E7L9F7K9G7J9 | E8L8E7L9F7K9G7J9 | E8L8E7L9F7K9G7J9 | E8L8E7L9F7K9G7J9 | E7L7E6L8F6K8G6J8 | E7L7E6L8F6K8G6J8 | E7L7E6L8F6K8G6J8 | E7L7E6L8F6K8G6J8 | H9H3G9J3G8J4G7J5 | H9H3G9J3G8J4G7J5 | H9H3G9J3G8J4G7J5 | H9H3G9J3G8J4G7J5 |
| Vcc | M | C8N8D4D12M4M12G3J13 | C8N8D4D12M4M12G3J13 | C8N8D4D12M4M12G3J13 | C8N8D4D12M4M12G3J13 | C7N7D3D11M3M11G2J12 | C7N7D3D11M3M11G2J12 | C7N7D3D11M3M11G2J12 | C7N7D3D11M3M11G2J12 | H11H1D2D10M2M10C7N5 | H11H1D2D10M2M10C7N5 | H11H1D2D10M2M10C7N5 | H11H1D2D10M2M10C7N5 |
| VccQ | M | C4N12D3M13E5L11F3K13K3F13L5E11M3D13N4C12N6C10 | C4N12D3M13E5L11F3K13K3F13L5E11M3D13N4C12N6C10 | C4N12D3M13E5L11F3K13K3F13L5E11M3D13N4C12N6C10 | C4N12D3M13E5L11F3K13K3F13L5E11M3D13N4C12N6C10 | C3N11D2M12E4L10F2K12K2F12L4E10M2D12N3C11N5C9 | C3N11D2M12E4L10F2K12K2F12L4E10M2D12N3C11N5C9 | C3N11D2M12E4L10F2K12K2F12L4E10M2D12N3C11N5C9 | C3N11D2M12E4L10F2K12K2F12L4E10M2D12N3C11N5C9 | D11M1C10N2E9L3C8N4C4N8E3L9C2N10D1M11F1K11 | D11M1C10N2E9L3C8N4C4N8E3L9C2N10D1M11F1K11 | D11M1C10N2E9L3C8N4C4N8E3L9C2N10D1M11F1K11 | D11M1C10N2E9L3C8N4C4N8E3L9C2N10D1M11F1K11 |

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| Vss | M | C5N11C7N9E3L13F5K11J3G13K5F11L3E13N5C11N7C9 | C5N11C7N9E3L13F5K11J3G13K5F11L3E13N5C11N7C9 | C5N11C7N9E3L13F5K11J3G13K5F11L3E13N5C11N7C9 | C5N11C7N9E3L13F5K11J3G13K5F11L3E13N5C11N7C9 | C4N10C6N8E2L12F4K10J2G12K4F10L2E12N4C10N6C8 | C4N10C6N8E2L12F4K10J2G12K4F10L2E12N4C10N6C8 | C4N10C6N8E2L12F4K10J2G12K4F10L2E12N4C10N6C8 | C4N10C6N8E2L12F4K10J2G12K4F10L2E12N4C10N6C8 | E11L1G11J1C9N3E8L4C5N7E4L8C3N9E1L11G1J11 | E11L1G11J1C9N3E8L4C5N7E4L8C3N9E1L11G1J11 | E11L1G11J1C9N3E8L4C5N7E4L8C3N9E1L11G1J11 | E11L1G11J1C9N3E8L4C5N7E4L8C3N9E1L11G1J11 |
| VREFQ\_0VREFQ\_1 | RR | nana | nana | D8M8 | D8M8 | nana | nana | D7M7 | D7M7 | nana | nana | H10H2 | H10H2 |
| VDDi | O | na | na | na | na | na | na | na | na | na | na | na | na |
| Vpp | O | C6D6M10N10 | C6D6M10N10 | C6D6M10N10 | C6D6M10N10 | C5D5M9N9 | C5D5M9N9 | C5D5M9N9 | C5D5M9N9 | F11F10K2K1 | F11F10K2K1 | F11F10K2K1 | F11F10K2K1 |
| CLE\_0CLE\_1 | MO | J7G9 | J7G9 | J7G9 | J7G9 | J6G8 | J6G8 | J6G8 | J6G8 | G5J7 | G5J7 | G5J7 | G5J7 |
| ALE\_0ALE\_1 | MO | K7F9 | K7F9 | K7F9 | K7F9 | K6F8 | K6F8 | K6F8 | K6F8 | G4J8 | G4J8 | G4J8 | G4J8 |
| WE\_0\_nWE\_1\_n | MO | G6J10 | nana | G6J10 | G6J10 | G5J9 | nana | G5J9 | G5J9 | F7K5 | nana | F7K5 | F7K5 |
| CLK\_0CLK\_1 | MO | nana | G6J10 | nana | nana | nana | G5J9 | nana | nana | nana | F7K5 | nana | nana |
| WP\_0\_nWP\_1\_n | MO | L7E9 | L7E9 | L7E9 | L7E9 | L6E8 | L6E8 | L6E8 | L6E8 | G3J9 | G3J9 | G3J9 | G3J9 |
| IO0\_0 / DQ0\_0IO1\_0 / DQ1\_0IO2\_0 / DQ2\_0IO3\_0 / DQ3\_0IO4\_0 / DQ4\_0IO5\_0 / DQ5\_0IO6\_0 / DQ6\_0IO7\_0 / DQ7\_0 | MMMMMMMM | L6K6L4K4F4E4F6E6 | L6K6L4K4F4E4F6E6 | L6K6L4K4F4E4F6E6 | L6K6L4K4F4E4F6E6 | L5K5L3K3F3E3F5E5 | L5K5L3K3F3E3F5E5 | L5K5L3K3F3E3F5E5 | L5K5L3K3F3E3F5E5 | F3F4D3D4D8D9F8F9 | F3F4D3D4D8D9F8F9 | F3F4D3D4D8D9F8F9 | F3F4D3D4D8D9F8F9 |

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| IO0\_1 / DQ0\_1IO1\_1 / DQ1\_1IO2\_1 / DQ2\_1IO3\_1 / DQ3\_1IO4\_1 / DQ4\_1IO5\_1 / DQ5\_1IO6\_1 / DQ6\_1IO7\_1 / DQ7\_1 | OOOOOOOO | E10F10E12F12K12L12K10L10 | E10F10E12F12K12L12K10L10 | E10F10E12F12K12L12K10L10 | E10F10E12F12K12L12K10L10 | E9F9E11F11K11L11K9L9 | E9F9E11F11K11L11K9L9 | E9F9E11F11K11L11K9L9 | E9F9E11F11K11L11K9L9 | K9K8M9M8M4M3K4K3 | K9K8M9M8M4M3K4K3 | K9K8M9M8M4M3K4K3 | K9K8M9M8M4M3K4K3 |
| DQS\_0\_tDQS\_1\_t | MO | nana | J4G12 | J4G12 | J4G12 | nana | J3G11 | J3G11 | J3G11 | nana | D5M7 | D5M7 | D5M7 |
| DQS\_0\_cDQS\_1\_c | RR | nana | nana | J5G11 | J5G11 | nana | nana | J4G10 | J4G10 | nana | nana | E5L7 | E5L7 |
| VSP0VSP1VSP2VSP3 | OOOO | M5D11M6D10 | M5D11M6D10 | M5D11M6D10 | M5D11M6D10 | M4D10M5D9 | M4D10M5D9 | M4D10M5D9 | M4D10M5D9 | E2L10F2K10 | E2L10F2K10 | E2L10F2K10 | E2L10F2K10 |
| ENiENo | OO | nana | nana | nana | nana | nana | nana | nana | nana | nana | nana | nana | nana |
| ZQ\_0ZQ\_1 | OO | nana | nana | D7M9 | D7M9 | nana | nana | D6M8 | D6M8 | nana | nana | G10J2 | G10J2 |
| DBI\_0DBI\_1 | OO | nana | nana | nana | J6G10 | nana | nana | nana | J5G9 | nana | nana | nana | F5K7 |

Table 2‑6 Signal mappings: BGA-178, BGA-154 and BGA-146 packages

Disposition log

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